

STATUS REQUEST		Docket Number CT00-020
In re Application of FU, CHIA-YU		
Application Number	09/712749	File Date 11-14-2000
Title LOW PROFILE INTEGRATED MODULE INTERCONNECTS AND METHOD OF FABRICATION		
Group Art Unit	3729	Examiner IRVIN DINGLE

Status of the above-identified application is requested.

The last paper(s) received by Applicant(s) was:

- Official Filing Receipt  
 Other Notice of Dismissed Petition mailed on December 3, 2007.

The last paper(s) submitted by Applicant(s) was:

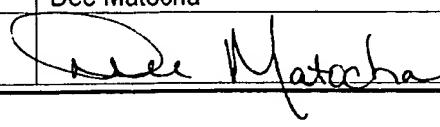
- Renewed Petition to Revive submitted on January 24, 2008.

Since the transmittal/receipt of said paper(s), no other communications have been received from the United States Patent and Trademark Office.

Attorney or Agent	Attorney		
Printed Name	DOLEZAL, DAVID G.	Registration Number	41,711
Signature			
Date	April 24, 2008		

#### CERTIFICATE OF MAILING, TRANSMISSION OR EFS FILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first-class mail in an envelope addressed to the Commissioner for Patents, Alexandria, VA 22313-1450; or facsimile transmitted to the U.S.P.T.O.; or electronically filed on the date below.

Name	Dee Matocha		
Signature		Date	April 24, 2008